

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

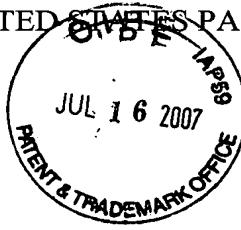
In re application of:

BURNS *et al.*

Appl. No.: 10/647,472

Filed: August 26, 2003

For: **Process Monitor for Monitoring
an Integrated Circuit Chip**



Confirmation No.: 2309

Art Unit: 2829

Examiner: Hollington, Jermele M.

Atty. Docket: 1875.3770001

Reply under 37 C.F.R. § 1.111

Mail Stop Amendment

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

In reply to the Office Action dated March 14, 2007, Applicants submit the following Listing of the Claims and Remarks.

The claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks and Arguments begin on page 7 of this paper.

It is not believed that extensions of time or fees for net addition of claims are required beyond those that may otherwise be provided for in documents accompanying this paper. However, if additional extensions of time are necessary to prevent abandonment of this application, then such extensions of time are hereby petitioned under 37 C.F.R. § 1.136(a), and any fees required therefor (including fees for net addition of claims) are hereby authorized to be charged to our Deposit Account No. 19-0036.